



10/814,568

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Vicki W. Tsai et al.	Examiner:	Stacy Whitmore
Serial No.:	10/814,568	Group Art Unit:	2825
Filed:	March 31, 2004	Docket:	80107.117US1
Title:	CONSTRAINTS-DIRECTED COMPILATION FOR HETEROGENEOUS RECONFIGURABLE ARCHITECTURES		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. . The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

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Serial No :10/814,568

Filing Date: March 31, 2004

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Dkt: P18383

Respectfully submitted,

VICKI W. TSAI ET AL.

By their Representatives,

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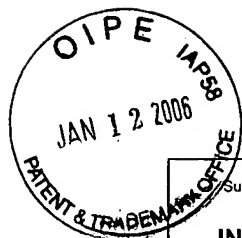
Date January 9, 2006

By Dana B LeMoine
Dana B. LeMoine
Reg. No. 40,062

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9 day of January, 2006.

Jane Sagers
Name

Jane Sagers
Signature



Substitute for form 1449A/PTO					
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/814,568
				Filing Date	March 31, 2004
				First Named Inventor	Tsai, Vicki
				Art Unit	2825
				Examiner Name	Whitmore, Stacy
Sheet	1	of	1	Attorney Docket No:80107.117US1	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date if Appropriate
		US-20050149890A1	07/07/2005	Tsai, V. W., et al.	12/29/2003
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FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
	WO-0038087A1	06/29/2000	Saul, J. M., et al.	
	WO-05098649A2	10/20/2005	Tsai, V., et al.	
	WO-05098649A3	10/20/2005	Tsai, V., et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.		T ²
		International Search Report and Written Opinion of the International Searching Authority; Dated 10/07/2005; PCT/US2005/010135, (14 pgs),		
		AHMADINIA, A., et al., "Temporal Task Clustering for Online Placement on Reconfigurable Hardware", <u>Field-Programmable Technology (FPT), 2003. Proceedings IEEE International Conference</u> , XP010688364,(Dec. 15, 2003),359-362		
		BLUME, H., et al., "Model-based Exploration of the Design Space for Heterogeneous System on Chip", <u>Proceedings of the IEEE International Conference on Application-Specific Systems, Architectures and Processors, 2002.</u> , XP010601457,(Jul. 17, 2002),29-40		
		PARAMESWARAN, S., et al., "Profiling in the ASP Codesign Environment", <u>Journal of Systems Architecture</u> , Elsevier Science Publishers BV, 46(14), XP004224625,(Dec. 01, 2000),1263-1274		
		SCIUTO, D., et al., "Metrics for Design Space Exploration of Heterogeneous Multiprocessor embedded Systems", <u>Proceedings of the 10th International Workshop on Hardware/Software Codesign</u> , XP002317354,(May 6, 2002),55-60		

EXAMINER

DATE CONSIDERED